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**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Craig Bingert et al.Application No./Patent No.: Not yet assigned Filed Issue Date: 6/20/03Entitled: Integrated Circuit And Associated Design Method Using Spare Gate Islands

Lattice Semiconductor Corporation, a Delaware Corporation  
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

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2. ☐ an assignee of less than the entire right, title and interest.  
 The extent (by percentage) of its ownership interest is \_\_\_\_\_ %  
 in the patent application/patent identified above by virtue of either:

A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 013116, Frame 0422, or for which a copy thereof is attached.

**OR**

B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

6/20/03  
 Date  
503-268-8629  
 Telephone number

Mark L. Becker  
 Typed or printed name  
[Signature]  
 Signature  
Associate General Counsel, IP  
 Title

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